



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.: 09/531,910

Group Art Unit: 2123

Filed: March 20, 2000

Examiner: H. Day

FOR: METHOD AND APPARATUS FOR MODELING AND
CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

RECEIVED

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.116

APR 27 2004

Commissioner for Patents
Mail Stop AF
P.O. Box 1450
Alexandria, VA 22313-1450

Technology Center 2100

Sir:

In response to the Office Action mailed January 20, 2004, please amend the
above-identified application as follows:

I hereby certify that this correspondence is being deposited with
the United States Postal Service as first class mail with sufficient
postage in an envelope addressed to Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313 on:

1. 20. 04

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